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"class processor" "coprocessor" public private

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The **coprocessor** is attached to a host computer, which provides storage, ... Tiny secure **memory** The TTP has space to read in and operate on only a constant ...

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protected CoffSection sections[]; **private** static final int headerLength = 20; ... @return the number of pages of physical **memory**. */. **public** int ...

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In both approaches, integrity is **protected** by binding the output back to a ... a message digest is normally signed with a **private** key of a **public** key ...

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[P n. See peta-. P5 n. Intel Corporation's internal working name ...](#)

a fixed-size block of **memory**. When used in the context of a paging **memory** ... Acronym for **private** Branch Exchange. an automatic telephone switching system ...

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... 0012 — Reserved ID for **private** key file ... PKI — **Public** Key Infrastructure ... PRO —

Protected command mode (Cryptoflex access condition) ...

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Thanks to {OS/2}'s new **memory** architecture, you no longer have to squeeze ... WTL4167

math **coprocessor**", numericalindex = "Frequency 3.3E+07 Hz; **Memory** size ...

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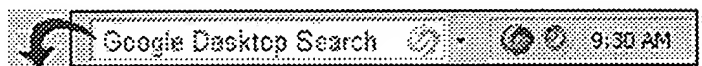
Chronology of Events in the History of Microcomputers Copyright (C ...

It uses a 16-bit data bus, 134000 transistors, and offers **protected** mode ... It features an 8088 processor, optional 8087 math **coprocessor**, 192KB RAM, ...
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L1	735	703/1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/17 09:15
L2	9971	"716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/17 09:15
L3	157	719/312.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/17 09:16
L4	1786	709/213-216.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/17 09:16
L5	0	((host core main controll\$3) adj2 processor with (communication\$3 adj2 (infrastructure structure))) and (((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) same (public near3 memory) same (private near3 memory) same (protected near3 memory)) and ((program\$3 adj2 interface\$1) near6 processor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:21
L6	0	((host core main controll\$3) adj2 processor with (communication\$3 adj2 (infrastructure structure))) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and (public near3 memory) and (private near3 memory) and (protected near3 memory) and ((program\$3 adj2 interface\$1) near6 processor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:22
L7	5	((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and (public near3 memory) and (private near3 memory) and (protected near3 memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:27

L8	0	((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and (public near3 memory near5 processor\$1) and (private near3 memory near5 processor\$1) and (protected near3 memory near5 processor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:28
L9	0	((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and (public near6 memory with processor\$1) and (private near6 memory with processor\$1) and (protected near6 memory with processor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:28
L10	579	((partition\$3 devid\$3) near5 (application\$1 program\$1) near6 (message\$1 function\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:30
L11	21	10 and ((host core main controll\$3) adj2 processor\$1 with communication)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:30
L12	110	10 and ((host core main controll\$3) adj2 processor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:31
L13	1	12 and (processor\$1 near5 (comm\$8 near3 (structure\$1 infra\$1structure)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:32
L14	0	1 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:32
L15	2	2 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:33
L16	7	12 and (((public private protected) near4 memory) with processor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:34

L17	0	12 and (public near4 memory) and (private near4 memory) and (protected near4 memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:35
L18	0	3 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:35
L19	2	4 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 09:35